

218357US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Yoshiaki SHIMOOKA et al

SERIAL NO.: 10/052,259

FILED: January 23, 2002

FOR: SEMICONDUCTOR DEVICE AND  
METHOD OF MANUFACTURING  
THE SAME

:

: GROUP ART UNIT: 2818

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: EXAMINER: Le

*8/Election*  
*Amend*  
*A*  
*Shimooka*  
*4-5-03*  
TC 2800 MAIL ROOM  
APR - 3 2003  
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AMENDMENT AND ELECTION

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Restriction Requirement dated February 28, 2003, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend Claims 1 and 4 shown in clean form below. A marked-up copy of amended Claims 1 and 4 is attached.

1. (Amended) A semiconductor device comprising:

a signal line, which is capable of passing a signal having a desired frequency  $f_0$   
therethrough, formed on a semiconductor substrate; and  
a differential signal line through which a signal in opposite phase to said signal passes,  
or which is connected to a ground power supply,

said signal line and said differential signal line being laminated via an insulating layer

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